

Ser. No. 10/759,000
Amdt. Dated April 1, 2011
Reply to Office Action of March 15, 2010

PF030026

Amendments to the Claims

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of the Claims

1. (Currently Amended) An electronic apparatus comprising:
 - a graphics memory storing a first and a second graphics object;
 - an OSD processor generating a first digital stream representing the first graphics object, ~~said OSD processor being unable to manage two graphic objects that overlap;~~
 - a picture memory containing a picture and generating a second digital stream from said picture;
 - ~~a mixer able to mix the first digital stream and the second digital stream into a video signal;~~
 - means for generating an overlap cue if an overlap is detected between the first and the second graphics objects; and
 - means for converting the second graphics object from said graphics memory into still picture data ~~if responsive to~~ said overlap cue indicating said overlap between the first and the second graphics object is generated; and
 - means for writing the still picture data to the picture memory; and
 - mixing means for mixing said first digital stream, generated by said OSD processor and representing said first graphics object, and said second digital stream, generated from said still picture data converted from said second graphics object from said graphics memory responsive to said overlap cue indicating overlap between the first and second graphics objects, into a video signal.
2. (Cancelled)

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3. (Currently Amended) An electronic apparatus according to Claim 1, comprising a means for controlling the ~~mixer~~ mixing means, means for conversion and means for writing as a function of the overlap cue.
4. (Currently Amended) An electronic apparatus according to Claim 1, comprising a video memory supplied by a decoder and linked to the ~~mixer~~ mixing means.
5. (Previously Presented) An electronic apparatus according to Claim 1, wherein the video signal is transmitted to an output connector.
6. (Previously Presented) An electronic apparatus according to Claim 1, wherein the means for converting the second graphics object into picture data are a piece of software executed by a main controller.
7. (Previously Presented) An electronic apparatus according to Claim 1, in which the picture memory is a stationary picture memory.
8. (Cancelled)
9. (Cancelled)

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10. (Currently Amended) A process for generating a video signal, comprising the following steps:

reception of a command to display a first and a second graphics object;
detection of a possible overlap between the first and the second graphics object;
if absence of overlap, generation by an OSD processor of a digital stream representing the first graphics object and the second graphics object, and generation of a video signal based on the digital stream said OSD processor being unable to manage two graphics objects that overlap;

if presence of an overlap:

- generation by said OSD processor of a first digital stream representing a first graphics object;
- conversion of the second graphics object into a still picture; writing of the still picture to a memory;
- generation of a second digital stream from said still picture in the memory;
- mixing of the first digital stream, representing said first graphics object and generated by said OSD processor, and of the second digital stream, generated from said still picture converted from said second graphics object from said graphics memory responsive to detection of an overlap between the first and second graphics objects; and
- generation of a video signal from said mixture.

11. (Cancelled)